

```

graph LR
    I/O[2 I/O DEVICE] <--> CPU[1 CPU]
    CPU <--> EXT[4 EXT. MEMORY]
    CPU <--> MAIN[3 MAIN MEMORY]
  
```

The diagram illustrates a computer system architecture. It consists of four main components: an I/O DEVICE (labeled 2), a CPU (labeled 1), EXT. MEMORY (labeled 4), and MAIN MEMORY (labeled 3). The I/O DEVICE is connected to the CPU via a bidirectional arrow. The CPU is connected to both the EXT. MEMORY and the MAIN MEMORY via bidirectional arrows.

```

graph TD
    B1([SEARCH PROCESS]) --> B2[/OBTAIN SEARCH TARGET RECORD/]
    B2 --> B3{IS THERE CORRESPONDING RECORD?}
    B3 -- YES --> B4[DATA DECOMPRESSION]
    B3 -- NO --> B5{SEARCH ALL RECORDS?}
    B5 -- YES --> B6[NOTICE FOR NO CORRESPONDING RECORD]
    B5 -- NO --> B1
    B4 --> B6
    B6 --> B7([END])
  
```

FIG.2

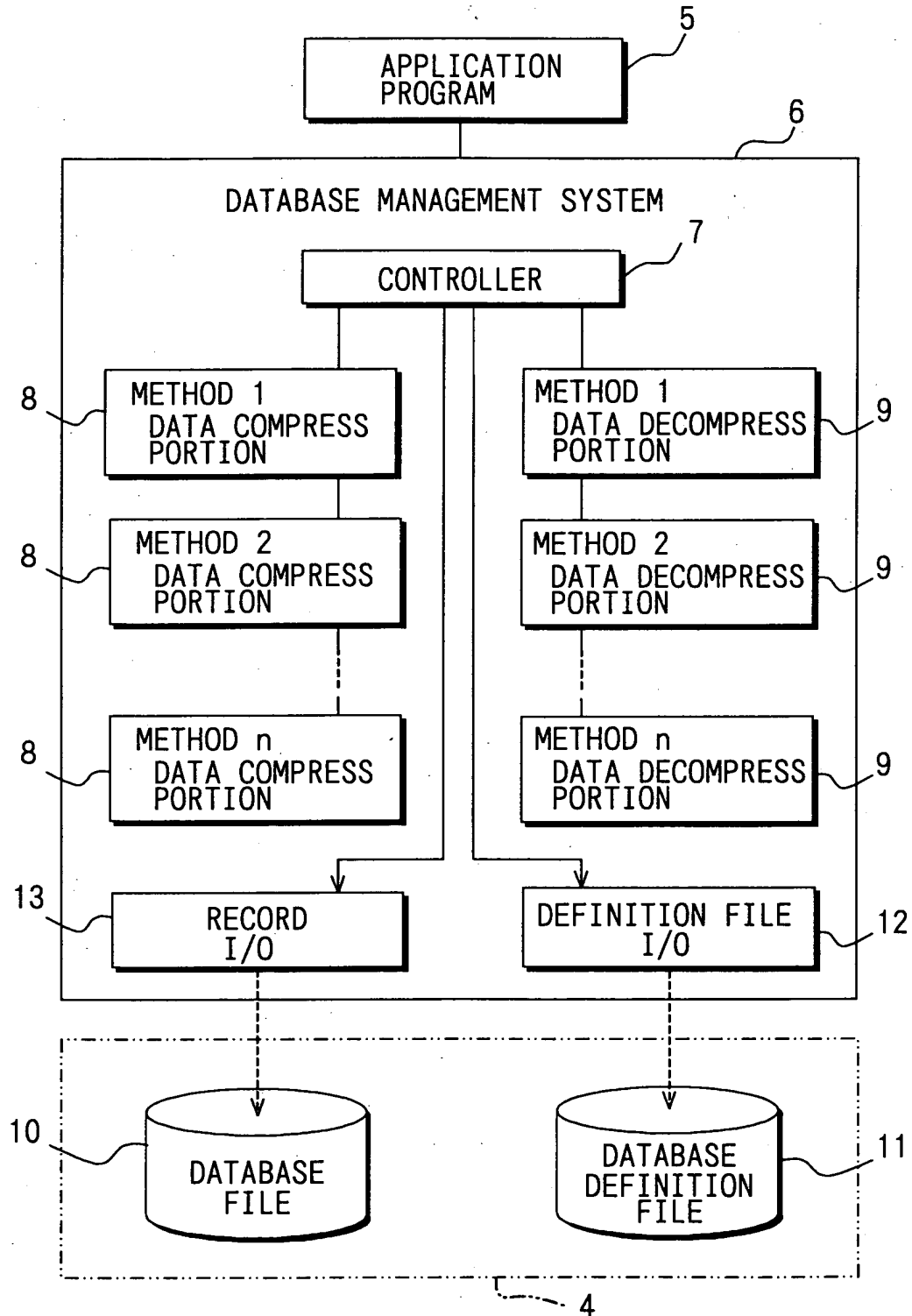
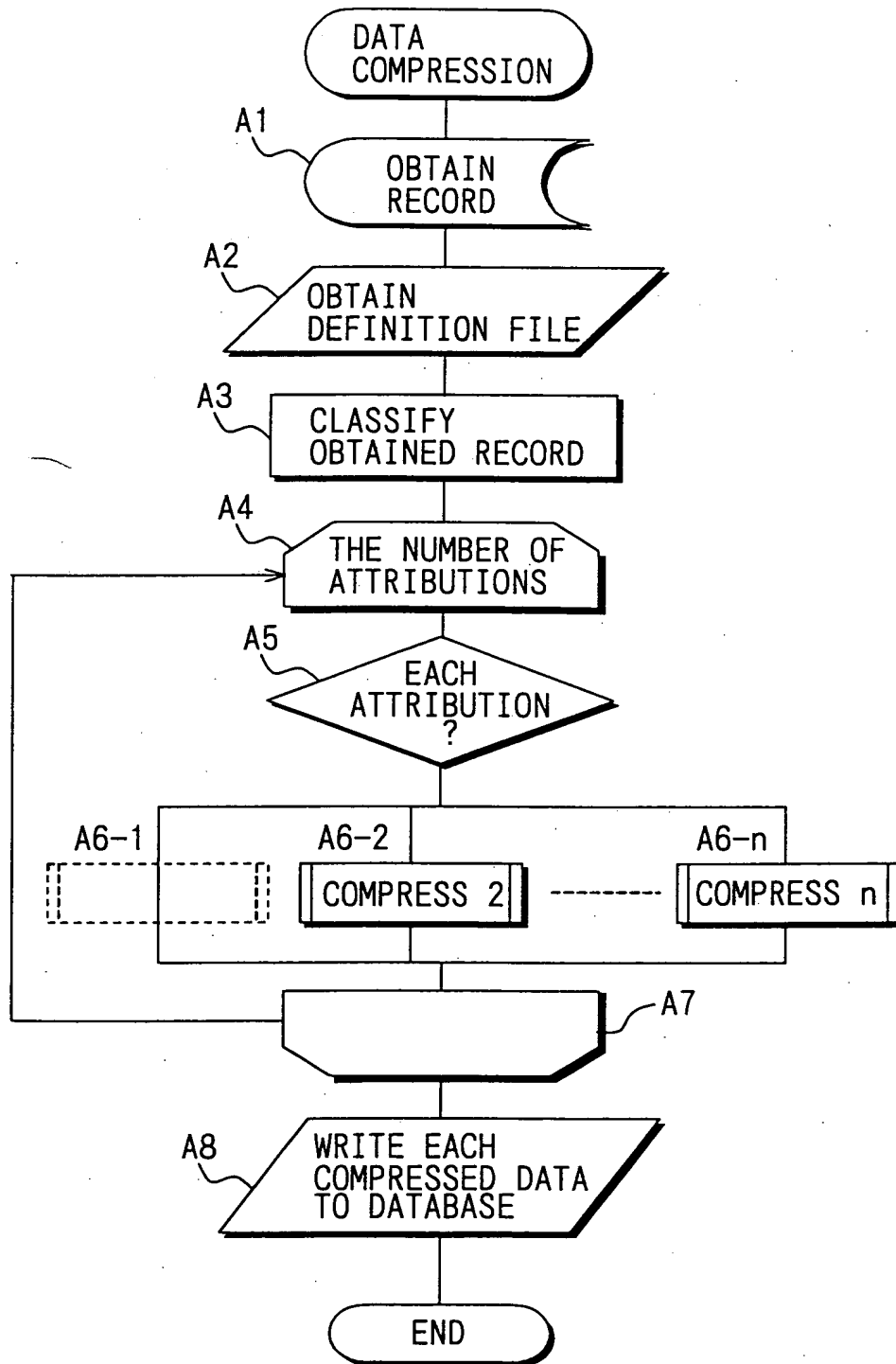


FIG.3



**FIG. 4A**

A	B	C	D	E
100	211156985	TOKYOABC__COFFEE	03-3587-5412	MINATO-KU, TOKYO
201	211579865	KAGAWAGET__BUGG	54-8574	TAKAMATSU-SHI, KAGAWA-PREF.
205	211879654	NAKAMURAUMAE__Corp.	052-778-5241	NAGOYA-SHI, AICHI-PREF.
∴	∴	∴	∴	∴
864	211522365	CHUKYOUUMMKOPO__TV	075-845-6523	KYOTO-SHI, KYOTO-PREF.
1000	211987654	IBARAGISHELLGAS	0725-68-4587	IBARAGI-SHI, OSAKA-PREF.
1054	211453285	RUMOIROWSONSTORE	66-6842	RUMOI-SHI, HOKKAIDO
∴	∴	∴	∴	∴

**FIG. 4B**

Ac	Bc	Cc	Dc	Ec
100	156985	04F5D234ED	7ED32D8	B674F7542DA532AD
201	579865	34986FED	9BC5342	4632FD742D5768C0
205	879654	9804576	5789365	F358D438F385D581
∴	∴	∴	∴	∴
864	522365	745FED	8796845	8523DFA6426D032C
1000	987654	EA65	84E5A65	3896F4DE6140F34D
1054	453285	9462984D	8E68B68	542DA642032CF757
∴	∴	∴	∴	∴

FIG.6

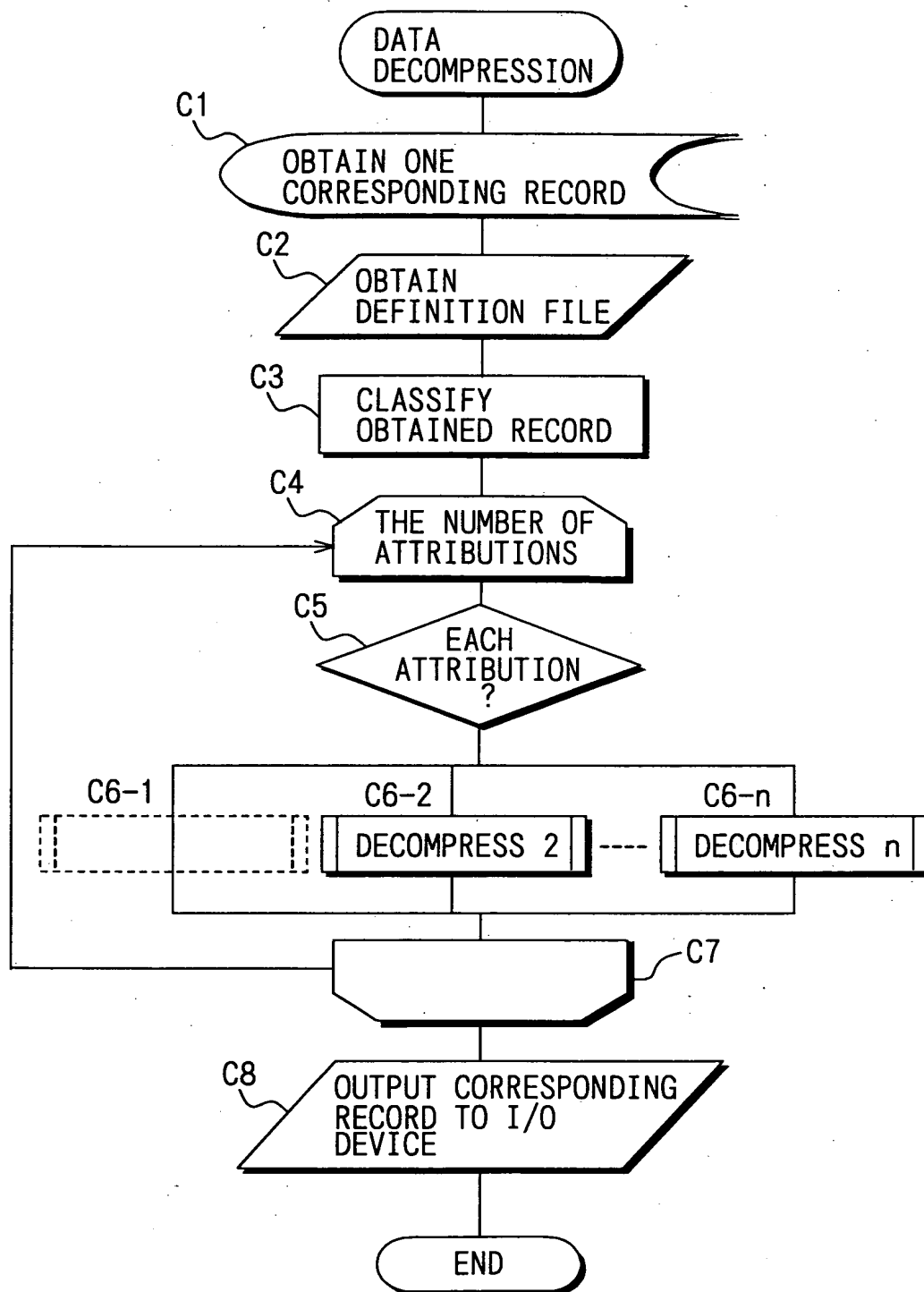


FIG.7

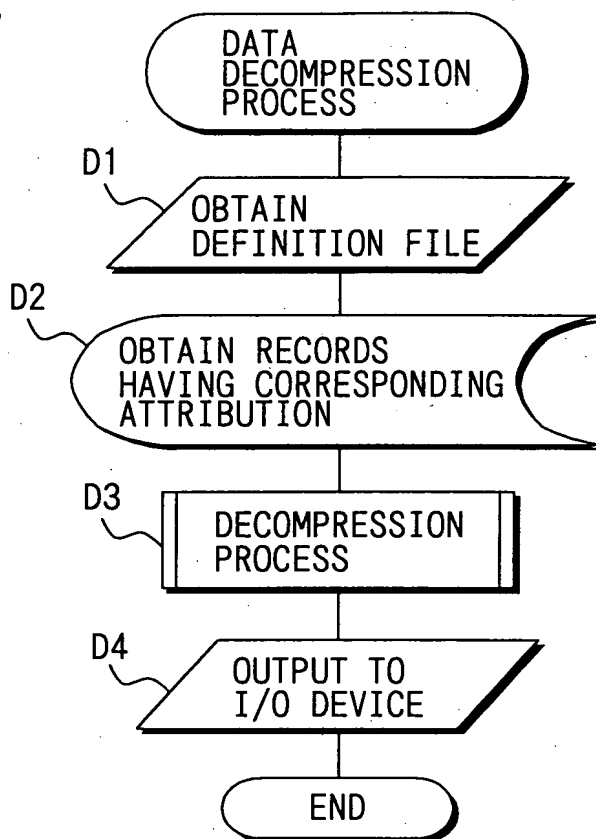
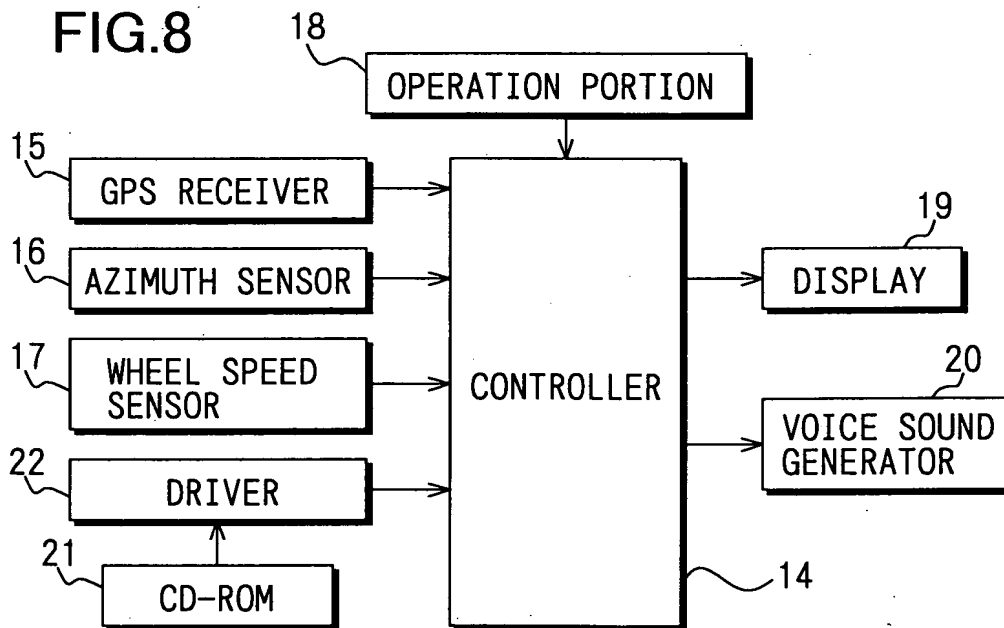
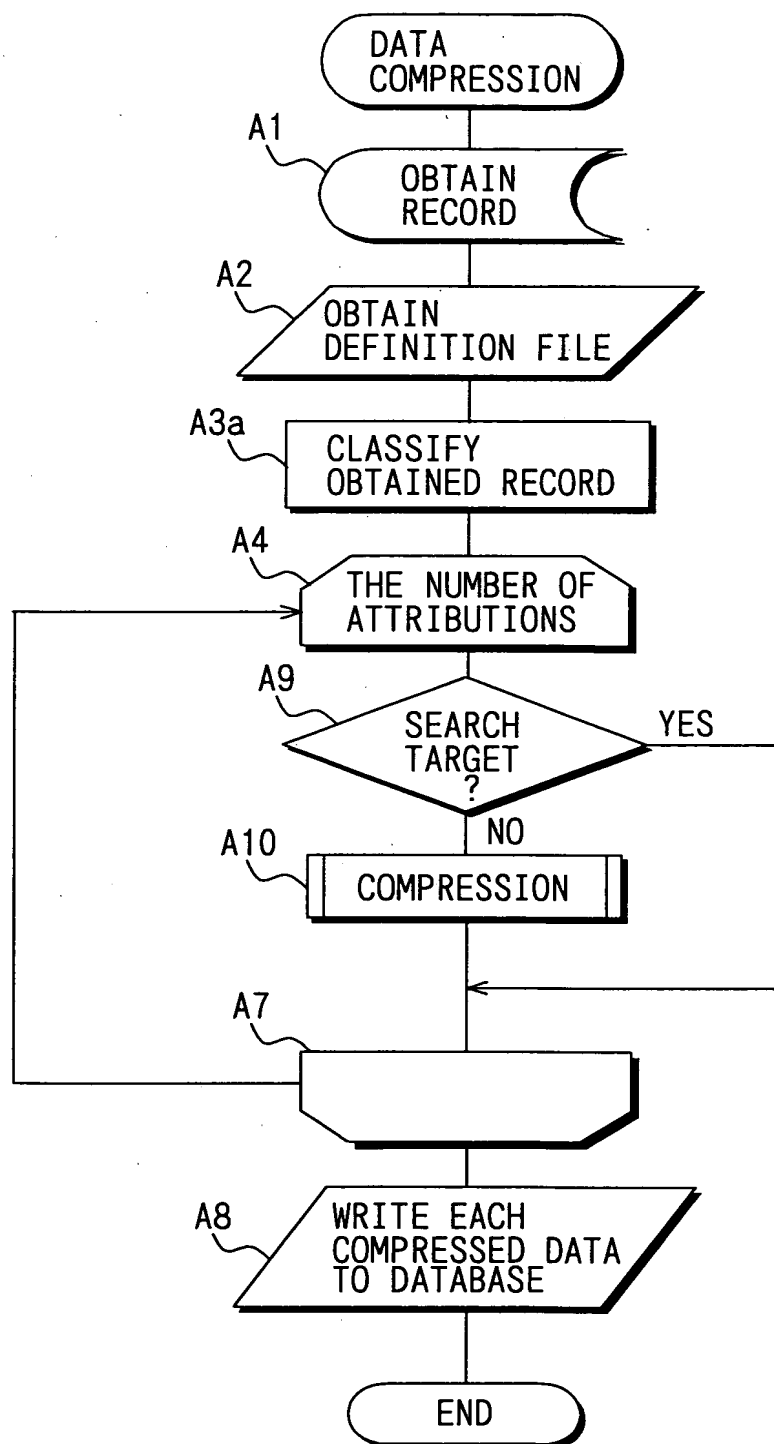


FIG.8



**FIG.9**



## DATA COMPRESSION

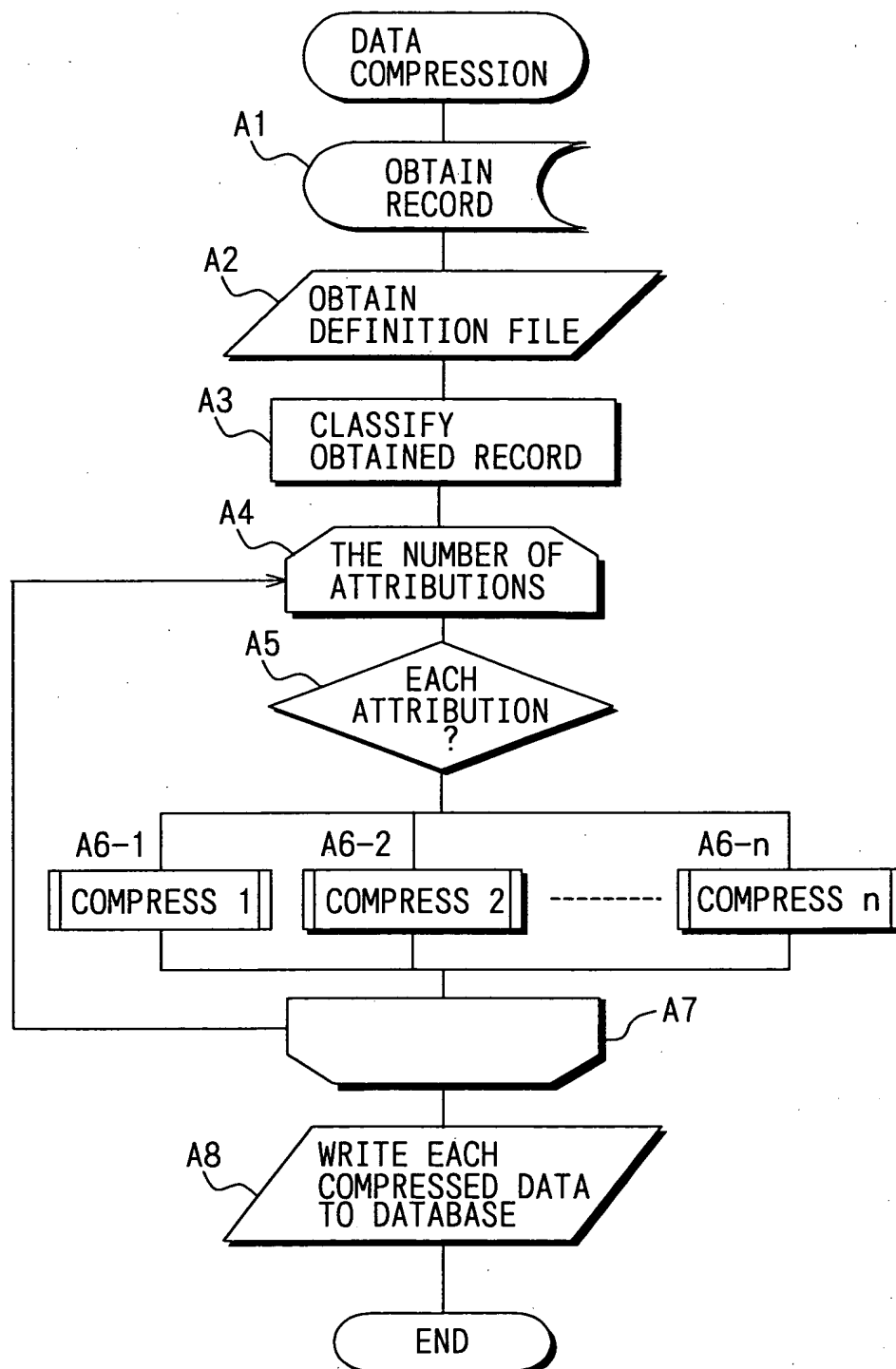




FIG.11

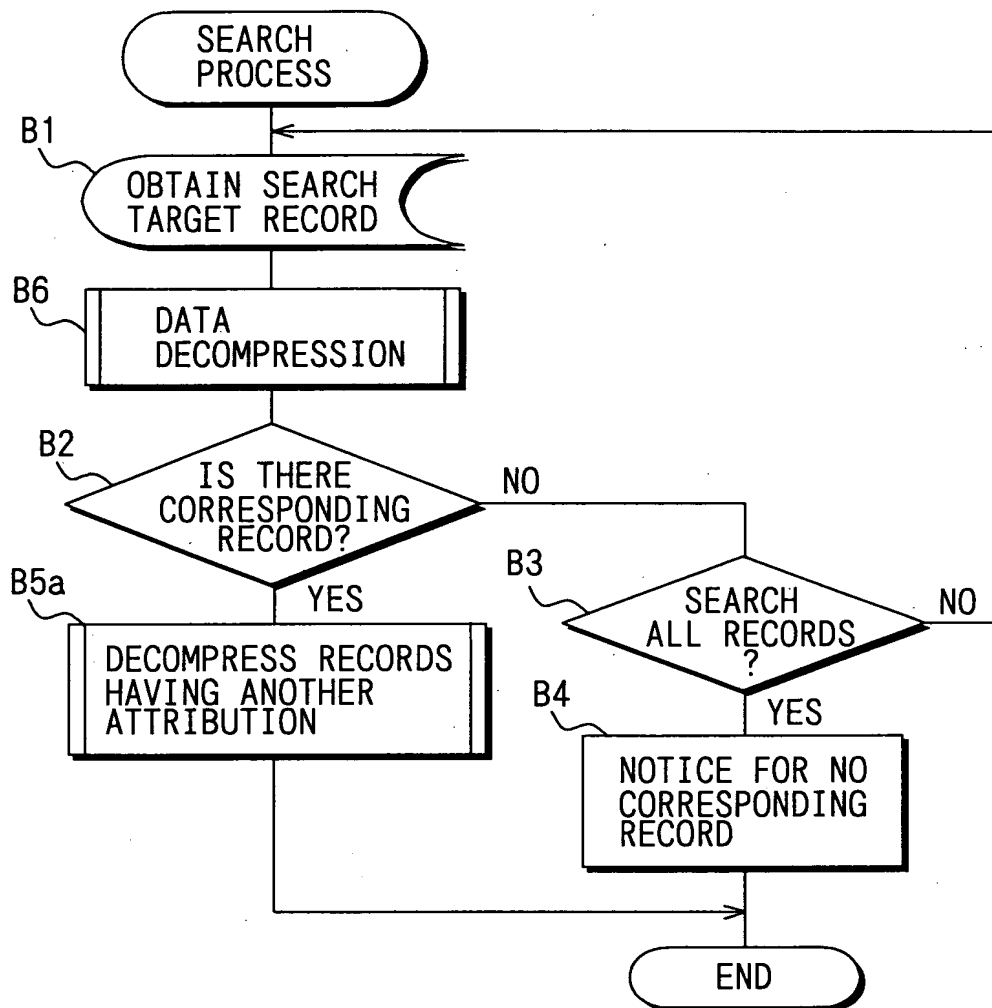


FIG.12

